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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/559,606	12/01/2005	Rintaro Takita	36856.1393	1512
54066 7590 11/28/2008 MURATA MANUFACTURING COMPANY, LTD. C/O KEATING & BENNETT, LLP			EXAMINER	
			DOUGHERTY, THOMAS M	
1800 Alexander Bell Drive SUITE 200 Reston, VA 20191		ART UNIT	PAPER NUMBER	
		2834		
			NOTIFICATION DATE	DELIVERY MODE
			11/28/2008	ELECTRONIC

# Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

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	Application No.	Applicant(s)				
	10/559,606	TAKITA, RINTARO				
Office Action Summary	Examiner	Art Unit				
	Thomas M. Dougherty	2834				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.  - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.  - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).						
Status						
1)⊠ Responsive to communication(s) filed on <u>07 No</u>	ovember 2008.					
<i>,</i> — · · · · · · · · · · · · · · · · · · ·	action is non-final.					
<i>;</i> —	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
	closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Disposition of Claims						
4)⊠ Claim(s) <u>6-12</u> is/are pending in the application.						
4a) Of the above claim(s) is/are withdrav	4a) Of the above claim(s) is/are withdrawn from consideration.					
5) Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>6-12</u> is/are rejected.						
7) Claim(s) is/are objected to.						
8) Claim(s) are subject to restriction and/or	election requirement.					
Application Papers						
9) The specification is objected to by the Examine	•					
10)⊠ The drawing(s) filed on <u>01 December 2005</u> is/are: a)⊠ accepted or b)□ objected to by the Examiner.						
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).						
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).						
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.						
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).						
a) All b) Some * c) None of:						
	1. Certified copies of the priority documents have been received.					
3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).						
* See the attached detailed Office action for a list of the certified copies not received.						
See the attached detailed Office action for a list of the certified copies not received.						
Attachment(s)						
1) Notice of References Cited (PTO-892)  4) Interview Summary (PTO-413)  Paper No(s)/Mail Date						
3) Information Disclosure Statement(s) (PTO/SB/08) 5) Notice of Informal Patent Application						
Paper No(s)/Mail Date 6) Other:						

#### **DETAILED ACTION**

### Response to Arguments

Applicant's arguments filed 11/07/08 have been fully considered but they are not persuasive. While the Applicant notes that it is not necessary to define what material is employed in his invention, and supports that with the citation of MPEP § 2173.05(g), he does not refute that a routineer in the art "would have no idea whether or not he would be infringing on the language of a patent that had the Applicant's language since any material that meets the characteristics is applicable." Indeed, if the Applicant's application were to be issued as a patent with the present claim language, even use of materials not yet ever used in semiconductor plate-shaped elements, and materials never considered by anyone including the Applicant for use as a semiconductor plate-shaped element, or perhaps a material or materials which have yet been constructed, e.g. alloys, elements of specific materials with specific doping requirements, stoichiometry, etc. would read on the issued patent. Consequently the Arguments are not persuasive.

The Applicant argues that Kaida does not specifically name the material of his frame members and therefore the rejection is not proper since it is not known whether or not they are conductive. As noted however the Applicant does not name the specific material of his components, which is just the reason he argues against the validity of applying the Kaida reference. Given this, the argument that Kaida does not read on the Applicant's claims is refuted by the Applicant's own logic. For these reasons, the rejection is maintained and in light of the Applicant's arguments an additional USC 112

rejection is made. *In arguendo,* for electrical connection to the outside, Kaida's tuning fork oscillator's electrodes must be energized through the frame members 35 and 36. It is understood from the figures of related art (figs. 3 and 4) shown by Kaida, that connection is through terminal electrodes and these are electrically connected to the electrodes on the resonator. For a secure electrical connection, it is necessary for the frame members to be conductive.

Concerning the Applicant's arguments of the Shinoda reference, these too are not persuasive. The Applicant maintains that this reference does not show frame shaped electrodes. The Examiner refers the Applicant to Shinoda's figures 2(A) and 2(B) in which frame shaped electrodes are clearly shown by reference numbers 11a and 21a in which gold (AU) is noted as the material employed.

### Claim Rejections - 35 USC § 112

The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

Claims 6-12 are rejected under 35 U.S.C. 112, first paragraph, because the best mode contemplated by the inventor has not been disclosed. Evidence of concealment of the best mode is based upon the Applicant's failure to disclose the material of the plate-shaped element.

The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

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Claims 6-12 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. The claims describe the characteristics of the plate-shaped element but never cite the material of that element. A routineer in the art would have no idea whether or not he would be infringing on the language of a patent that had the Applicants' language since any material that meets the characteristics is applicable.

## Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 6-12, as best understood, are rejected under 35 U.S.C. 103(a) as being unpatentable over Kaida (US 5,302,880) in view of Shinoda (JP 57-60717). Kaida shows (fig. 1) an electronic component device comprising: a rectangular plate-shaped element (1) including a functional part and a first frame-shaped electrode (35) surrounding the functional part, wherein, as best understood, the coefficient of linear expansion in the x direction along a side of the rectangle is different from the coefficient of linear expansion in the y direction orthogonal to the x direction in the rectangular plane (note that as Kaida shows the claimed structural features, the functionality is regarded as being met as well); a substrate (38) including a second frame-shaped electrode (36) arranged on a front face of the substrate (38) at a position so as to correspond to the first frame-shaped electrode (35); and each of the first frame-shaped

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electrode (35), the second frame-shaped electrode (36), includes a strip-shaped portion extending in the x direction and a strip-shaped portion extending in the y direction; the element (1) and the substrate are bonded together, the functional part provided on the element (1) is hermetically sealed in a space formed between the element (1) and the substrate (38); and when the difference in expansion in the x direction between the element and the substrate is represented by Qx and the difference in expansion in the y direction between the element and the substrate is represented by Qy, as best understood, in each of the first frame-shaped electrode (35), the second frame-shaped electrode (36), a width of the strip-shaped portion extending in the direction having the larger difference in expansion is smaller than a width of the strip-shaped portion extending in the direction having the strending in the direction having the smaller difference in expansion.

As best understood, when the coefficient of linear expansion in the x direction of the substrate is represented by  $A_x$ , the coefficient of linear expansion in the y direction of the substrate is represented by  $A_y$ , the coefficient of linear expansion in the x direction of the element is represented by  $B_x$ , the coefficient of linear expansion in the y direction of the element is represented by  $B_y$ , the length of the external side of the stripshaped portion extending in the x direction of the first and second frame-shaped electrodes is represented by  $d_x$ , the length of the external side of the strip-shaped portion extending in the y direction of the first and second frame-shaped electrodes is represented by  $d_y$ , the difference  $d_x$  in expansion is represented by  $d_y = d_x - d_y = d_y$  and  $d_y = d_y - d_y = d_y$ . (mm/°C), and the difference  $d_y$  in expansion is represented by  $d_y = d_y - d_y = d_y$ .) In the larger difference in expansion is about 2.2 x  $d_y = d_y - d_y$ .

As best understood, when the ratio of flexural rigidity in the x direction between the element and the substrate is represented by  $R_x$  and the ratio of flexural rigidity in the y direction between the element and the substrate is represented by  $R_y$ , the larger ratio of the flexural rigidity ratios  $R_x$  and  $R_y$  is about 1.2 or less.

The element (1) is a high frequency element.

Kaida, as noted, shows (fig. 1) an electronic component device comprising: a rectangular plate-shaped element (1) including a functional part and a first frameshaped electrode (35), wherein the coefficient of linear expansion in the x direction along a side of the rectangle is different from the coefficient of linear expansion in the y direction orthogonal to the x direction in the rectangular plane; and a substrate (38) including a second frame-shaped electrode (36); wherein each of the first frame-shaped electrode (35) and the second frame-shaped electrode (36) includes a strip-shaped portion extending in the x direction and a strip-shaped portion extending in the y direction; the element and the substrate are bonded together with the functional part provided on the element hermetically sealed (col. 8, lines 10-13) in a space formed between the element (1) and the substrate (38); and when the difference in expansion in the x direction between the element and the substrate is represented by Q<sub>x</sub> and the difference in expansion in the y direction between the element and the substrate is represented by Q<sub>v</sub>, in each of the first frame- shaped electrode and the second frameshaped electrode, a width of the strip-shaped portion extending in the direction having the larger difference in expansion is smaller than a width of the strip-shaped portion extending in the direction having the smaller difference in expansion.

Kaida does not note a solder sealing frame or a surface acoustic wave device.

Kusabiraki et al. teach (ABSTRACT) use of solder for sealing an electronic package containing a piezoelectric resonator, which description includes both high frequency devices and surface acoustic wave devices.

Kusabiraki et al. don't note electrode frames or note the thickness of the solder sealing frame is about 18  $\mu m$  or more.

It would have been obvious to one having ordinary skill in the art to employ the solder material of Kusabiraki et al. in the device of Kaida since this is an excellent design to allay problems of thermal shock as noted in the Abstract.

Concerning the thickness of the solder in the combined device, it would have been obvious to one having ordinary skill in the art to employ a thickness of 18  $\mu$ m or more for the solder layer in a combined device of Kusabiraki et al. and Kaida since it has been held that where the general conditions of a claim are disclosed in the prior art, discovering the optimum or workable ranges involves only routine skill in the art. *In re Aller*, 105 USPQ 233.

#### Conclusion

THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not

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mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Direct inquiry to Examiner Dougherty at (571) 272-2022.

/T. M. D./ /Thomas M. Dougherty/

tmd Primary Examiner, Art Unit 2834

November 20, 2008